



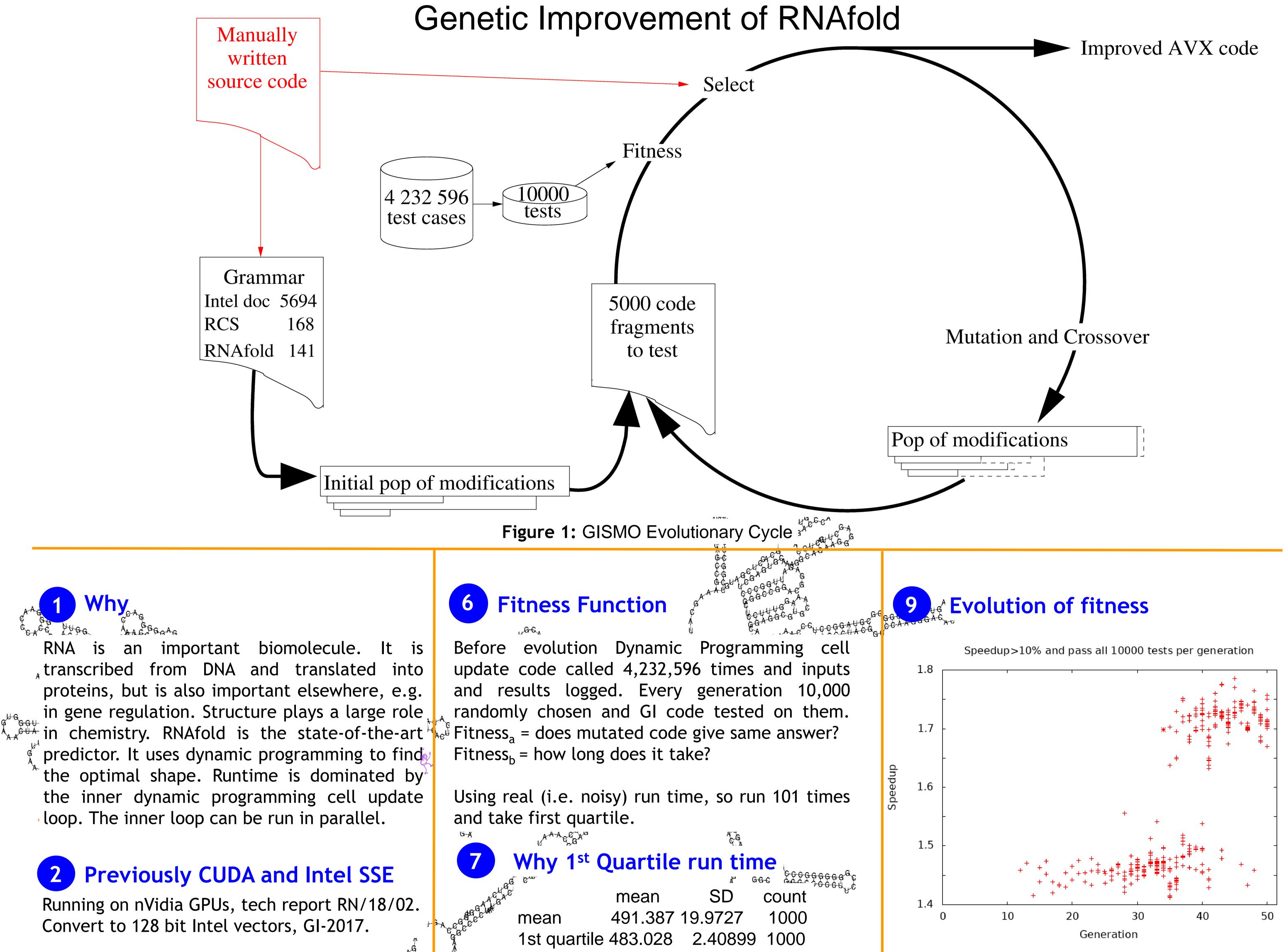
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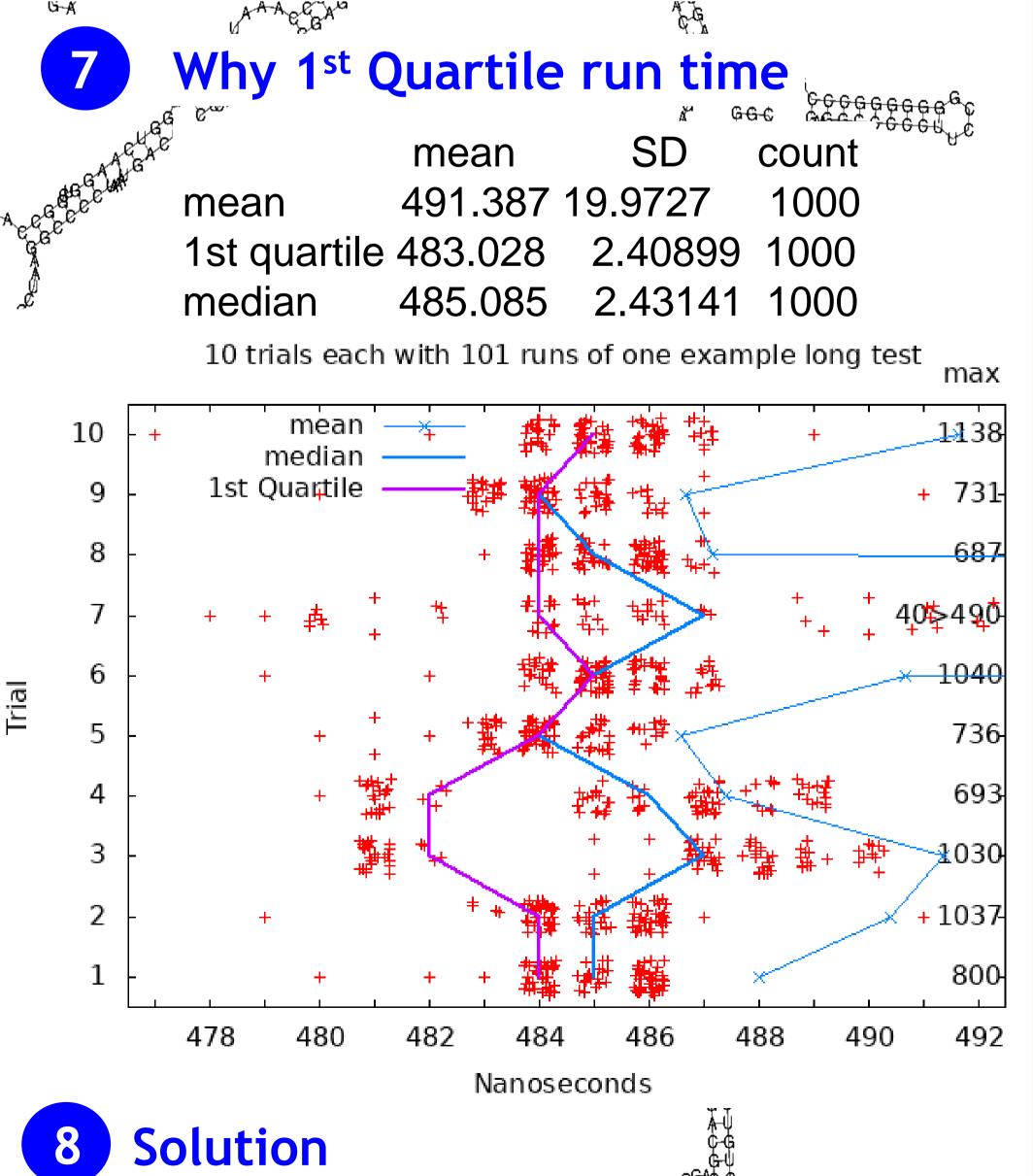
Evolving AVX512 Parallel C Code using Genetic Programming



3 Now 512 bit Intel AVX

Take existing SSE code, revision history, Intel documentation and test suite. Evolve 6.1 faster than sequential code. GI code incorporated into ViennaRNA package since Dec 2018.

RNA STRAND Test on 4666 known RNA structures.





On a standard Intel Xeon Gold 6126 2.60GHz server, with an unmodified version of Linux, evolution can optimise a critical function written in C. Automatically creating AVX instructions, genetic improvement gives almost a doubling in speed (1.71 fold) on top of hand written SSE instructions (6.1 times the sequential code). This GI code in ViennaRNA

GGGP to **improve RNAfold**

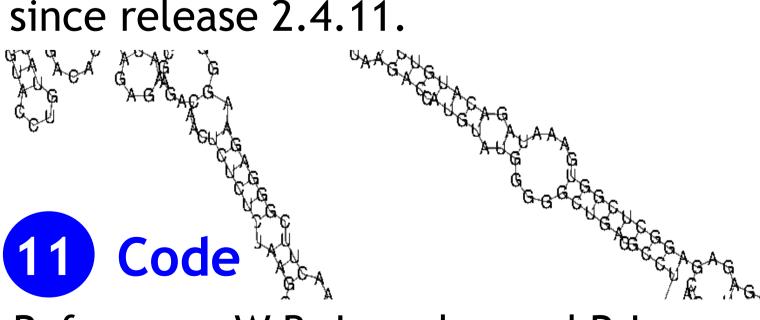
Representation: variable list of replacements, deletions and insertions into BNF grammar (6122 rules).

Fitness: Compile (GCC 7.3.1), run on 10 000 random test cases. Two objectives: number of tests past and elapsed time.

5000, panmictic, Population: elite 10, generational.

Parameters: Initial population of random single mutants. 50% truncation selection. 50% two point crossover, 50% mutation. No size limit. Stop after 50 generations.

Best individual in population at generation 34 contained 8 mutations, hill climbing leaves 3: •vecsize=16 converts generic code to 512 bits •Line 8506 of the Intel documentation is _mm512_reduce_min_epi32, so en=_mm_cvtsi128_si32(min4) becomes en=_mm512_reduce_min_epi32(min4). •The remaining mutation increases efficiency.



Reference: W.B. Langdon and R.Lorenz. Evolving AVX512 Parallel C Code using GP. In L.Sekanina, T.Hu and N.Lourenco editors, EuroGP 2019, Leipzig. Springer Verlag.

GISMO

http://www.cs.ucl.ac.uk/staff/W.Langdon/ftp/gp-code/rnafoldAVX.tar.gz

Background predicted structure of RNA³ from Spinach 23S Ribosome CRW_01456